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IN THE


UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Mike Hong et al.  
SERIAL NO.: 09/313,857  
FILING DATE: May 18, 1999  
TITLE: System and Method for Implementing a Two-Layer Z-Range Buffer  
EXAMINER: Almis R. Jankus  
GROUP ART UNIT: 2671  
CONF. NO.: 4384  
ATTY.DKT.NO.: PA1715US

CERTIFICATE OF MAILING

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Dated: 9/3/02

  
Susan Yee

COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

LETTER TO THE CHIEF DRAFTSMAN

SIR:

Subject to the approval of the Primary Examiner in the above-entitled patent application, please substitute the enclosed nineteen (19) sheets of corrected formal drawings containing Figures 1 through 16 for the nineteen (19) sheets of drawings containing Figures 1 through

6/02

16 previously submitted.

Respectfully submitted,

Mike Hong et al.

Dated: 9/3/02

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